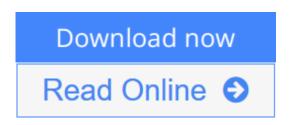


# Advanced Computer Architectures: A Design Space Approach (International Computer Science Series)

By Dezso Sima, Terence Fountain, Peter Karsuk



Advanced Computer Architectures: A Design Space Approach (International Computer Science Series) By Dezso Sima, Terence Fountain, Peter Karsuk

This timely book provides an unconventional and up-to-date overview of all the important computer architectures and is one of the first texts to present all the relevant concepts of advanced architecture classes by exploring their design spaces.

Advanced Computer Architectures will prove an indispensable guide for anyone who needs to be acquainted with the relevant concepts and solutions introduced in recent years to the dramatically changing world of computer architecture. For the student of advanced level courses in computer architecture, this book will provide a comprehensive and accessible overview of the subject whilst its strong practical orientation will make it an invaluable reference for the practitioner.

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# Advanced Computer Architectures: A Design Space Approach (International Computer Science Series) By Dezso Sima, Terence Fountain, Peter Karsuk Bibliography

- Sales Rank: #2557033 in Books
- Brand: Brand: Addison-Wesley
- Published on: 1997-07-07
- Ingredients: Example Ingredients
- Original language: English
- Number of items: 1
- Dimensions: 9.50" h x 1.63" w x 7.00" l, 2.81 pounds
- Binding: Paperback
- 800 pages

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# **Editorial Review**

From the Back Cover

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## **Features:**

- Explores design spaces for each architecture class and exposes evolution of concepts and design issues
- Provides an up-to-date overview of significant architecture classes, including unique in-depth coverage of superscalar architectures as well as multithreaded, shared and distributed memory MIMDs, and associative and neural architectures
- Identifies which concepts and design choices have been made use of in important processors and illustrates significant trends and surpassed and viable concepts
- Case studies and tables show microarchitectural details of relevant processors, including the PentiumPro, PowerPC 604, PowerPC 620 and R10000, allowing comparisons between them

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About the Author

**Dezsö Sima** is Director of Informatics at the Kandó Polytechnic in Budapest where he has specialized in computer architecture since 1972. He has published two books and over thirty papers.

**Terence Fountain** is a Doctor of Physics and Astronomy at University College London and has previously published in the field of computer architecture.

**Péter Kacsuk** is Head of the Laboratory of Parallel and Distributed Systems at the KFI-MSZKI Research Institute of the Hungarian Academy of Sciences.

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